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1400-1900 GHz MEMBRANE BASED SCHOTTKY DIODE TRIPLERS

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Abstract

This paper will discuss the design of membrane based planar Schottky diode triplers working in the 1400 to 1900 GHz range. The optimized design of the chip is intricately related to the applicable technology and the available input power. A detailed and systematic discussion regarding balanced versus un-balanced designs for triplers will be presented.

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