

FPGA PLATFORMS AND THE TOOLFLOW

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This talk will introduce the FPGA platforms we developed, including SNAP2 and two kintex 7 platforms. All of them have been ported to casper toolflow. Three kinds of ADC yellow blocks (E2V_5G, HMCAD1520 and ADS5407) are also built. Besides, experiences in multi-clock design will be shared, which is important to optimize timing especially when resource utilization is very high.